Please replace the paragraph starting at line 16, page 7 with the following new paragraph:

Similarly, a reset signal may occur while the modem 100 is in a receive mode. For example, the modem 100 may receive only bad cyclic redundancy check packets. In such a case, the modem 100 halts the receive interface and sets a receive-reset bit. The modem 100 then flushes and realigns the receive buffers and reprograms the necessary buffers. The modem 100 then restarts the receive interface, thus completing the reset operation.

Please replace the paragraph starting at line 8, page 15 with the following new paragraph:

Figure 2 illustrates a parameter storage module 933. The parameter storage module 933 contains channel characteristics, in the form of taps, corresponding to each remote site. In one embodiment, each remote site has different tap values, each of which corresponds to a recent communication parameter by the remote site. Since each remote site has different channel characteristic, each remote site's tap values must be stored and retrieved independently. The digital equalization module 930 receives, from the parameter storage module 933, the tap values corresponding to the remote site that is presently sending an uplink burst to the modem 100. Once received, the digital equalization module 930 sets the correct channel characteristics for the incoming burst from the remote site which helps the modem 100 perform by keeping estimates of the channel characteristics for each transmission from each remote site. In one embodiment, the acquisition, tracking and modulation control module 918 generates an index of remote sites and their associated tap values to expedite their retrieval from the parameter storage module 933. After receiving a burst from one of the remote sites, the parameter storage module 933 updates the stored tap values for that remote site.

IN THE CLAIMS:

Please cancel claims 27, 86-89, and 94 without prejudice.

Please amend claims 26, 57, 72-74, 76, 90, and 91 as follows:

-2-